

# ADI SIREESHA

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## Objective

Detail-oriented firmware engineer with extensive experience in SSD support, AI model inference, and BMS project development using Zephyr RTOS, seeking to leverage technical skills in a dynamic role. Aimed at contributing to innovative projects and driving results through expertise in embedded systems, hardware communication protocols, and software development, hands on experience with compilation of linux in RPI,BeagleBone.

## Professional Experience

**Firmware engineer**,Capgemini Engineering service Ltd,,Bangalore(**may 2024- present**)

- Worked to write application code on Silicon Board external flash interface with SPI protocol Using ZephyrRTOS.
- Hands on compilation procedure and updating the u-boot versions on FREE-BSD OS on BeagleBone Black and RPI-4B.
- Hands On U-boot version updating for IPQ9574 Board.
- Worked on Bring-up the SPI-protocol in RPI-4B board using the FREE-RTOS.
- Bare metal programming in STM32F4 series Discovery board to implemented the GPIO<sub>driver</sub>development.

**Embedded Firmware Engineer**, Gyan Samhita Tech Pt. Ltd., Hyderabad (**April 2022 - may 2024**)

- Spearheaded the development of firmware for SSD support.
- Led hardware communication initiatives using UART, I2C, and SPI protocols, enhancing device connectivity.
- Drove AI model inference projects on platforms such as Neural Stick and Jetson Nano.
- Collaborated on the BMS project using Zephyr RTOS, successfully implemented the logging module api to store logs on sd card.

**Junior Lecturer**, APT Polytechnic College (**2018-2020**)

- Delivered lectures in Embedded Systems and Programming Languages, achieving.
- Developed and implemented practical lab exercises, enhancing students' hands-on experience with real-world applications of firmware engineering principles.

## Skills

<b>Programming Languages</b>	: Embedded C, Python
<b>Operating Systems</b>	: Zephyr RTOS,FREE-RTOS
<b>Protocols</b>	: I2C, SPI, UART,TFTP ptotocol
<b>Tools</b>	: VS Code, PyCharm, Eclipse, Keil U
<b>Hardware Tools</b>	: Oscilloscope, Logic Analyzer
<b>Version Control</b>	: Git

## Projects

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**Project** : Solid State Drive  
**Client** : ATP  
**Description** : Implementation of firmware for solid state drive  
**Role** : Team member  
**Team Size** : 3 members  
    **Language** : Embedded C  
    **Development Tool** : Eclipse  
**Environment** : **Controller** : SMI-2259  
    **Communications** : SATA  
**Responsibilities** : Testing the modules as per requirement and implementing the API's for functionality of modules in SSD.

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**Project** : Battery Monitoring System  
**Client** : self  
**Description** : Monitoring the SOC, SOH, SOL and balancing the Individual lithium cells under charging and discharging to provide information on the LCD Display and extend the life the battery pack  
**Role** : Team member  
**Team Size** : 2 members  
    **Language** : Embedded C  
    **Development Tool** : Eclipse  
**Environment** : **Controller** : STM32H753ZI - Arm Cortex M7  
    **Communications** : UART  
**Responsibilities** :

- Initialization of SD CARD.
- Implementing the log module to store the logs on sd cards.
- Handling the logs when the size of log limits the threshold value set by the user

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## Education

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- B.Tech in EEE, Aditya Engineering College, Surampalem, AP, 2016
- Diploma in EEE, Aditya Polytechnic College, Surampalem, AP, 2013
- SSC, APSWR School for Girls, Pithapuram, AP, 2010

## Personal Details

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Father's Name: Adi Srinivas  
Mother's Name: Kondamma  
Nationality: Indian  
Hobbies: Playing badminton  
Languages Known: Telugu, English, Hindi

## Declaration

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I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above-mentioned particulars.

**Date** : 9-12-2024  
**Place** : BANGALORE

